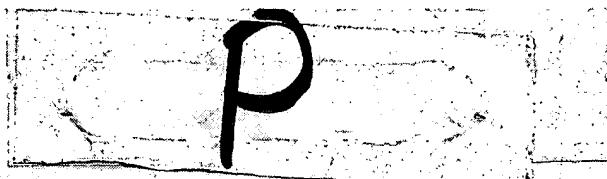


11000 U.S. PTO
09/974806
10/12/01

Subclass	Class	ISSUE CLASSIFICATION



PATENT NUMBER

U.S. UTILITY Patent Application

JCS88

O.I.P.E.	PATENT DATE
EM SCANNED	NSI G.W. Q.A.

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/974806	D F	345	211	2674	DIN H, DUC
APPLICANTS Shigetoshi Tomio Naoki Matsui Shinpei Yao					
TITLE Flat display panel having internal lower supply circuit for reducing power consumption					
PTO-2040 12/99					

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ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

<input checked="" type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. 6,500,314	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			Amount Due	Date Paid
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